

EXPRESS MAIL LABEL NO. EL746146730US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

: Atty. Docket: POU920010019US1

Jason D. HIBBELER et al.

: Applications Branch

Serial No.: (Not yet assigned)

Filed: (Herewith)

For: *DYNAMIC CPU USAGE PROFILING AND FUNCTION CALL TRACING*



INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents  
Washington, D. C. 20231

SIR:

The attached Form PTO-1449 provides a listing of information which may be relevant to the subject application. This IDS is not intended as a representation that better art is not available, nor that the information provided is prior art.

This IDS is submitted under:

- ☒ 37 CFR 1.97(b) - No Fee.  
☐ 37 CFR 1.97(c) - No Fee, with Certification.  
☐ 37 CFR 1.97(c) - Fee.  
☐ 37 CFR 1.97(d) - Fee, Certification & Petition.

The Commissioner is authorized to charge any required fees under 37 CFR 1.17(p) and (i) (1) to Deposit Account No. **09-0463**

Respectfully submitted,

Date: 8/23/01

By: 

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Form PTO-1449

U.S. Dept. of Commerce  
Patent & Trademark Office

Atty. Docket: POU920010019US1

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List of Documents  
Cited by Applicant

(Use several sheets if necessary)

Applicant: Jason D. HIBBELER et al.

Filing Date: Herewith

Group: (not yet assigned)

## U.S. PATENT DOCUMENTS

Ex'rs In'l		Document Number	Date	Name	Class	Sub- class	Filing Date, if applicable
	AA1	5,121,501	June 9, 1992	Baumgartner, et al.			
	AA2	5,347,649	September 13, 1994	Alderson			
	AA3	5,355,487	October 11, 1994	Keller, et al.			
	AA4	5,446,876	August 29, 1995	Levine, et al.			
	AA5	5,594,864	January 14, 1997	Trauben			
	AA6	5,664,095	September 2, 1997	Cox, et al.			
	AA7	5,761,091	June 2, 1998	Agrawal, et al.			
	AA8	5,797,115	August 18, 1998	Fuller			
	AA9	5,964,893	October 12, 1999	Circello, et al.			
	AB1	6,086,618	July 11, 2000	Al-Hilali, et al.			

## FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Sub- class	Trans'n Yes/No
	AB2	JP6342386A	September 2, 1992	Japan			Yes (Abstract)

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	AB3	IBM Technical Disclosure Bulletin, "Processor Architecture for Measurement and Monitoring Functions," Vol. 32, No. 9A, February 1990.
	AB4	IBM Technical Disclosure Bulletin, "Process for Real-Time, Trace-Driven Performance Monitors," Vol. 34, No. 5, October 1991.
	AB5	IBM Technical Disclosure Bulletin, "Programmable Performance Monitor Apparatus for a Multi-Chip Super Scalar Processor, Vol. 37, No. 04B, April 1994.
	AB6	IBM Research Disclosure No. 42898, "Automated Inclusion of Program Tracing Statements," December 1999.
	AB7	IBM Research Disclosure No. 431, "Dynamic Monitoring of System Parameters," March 2000.

Examiner:

Date Considered:

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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